

FIG 1

State of the art

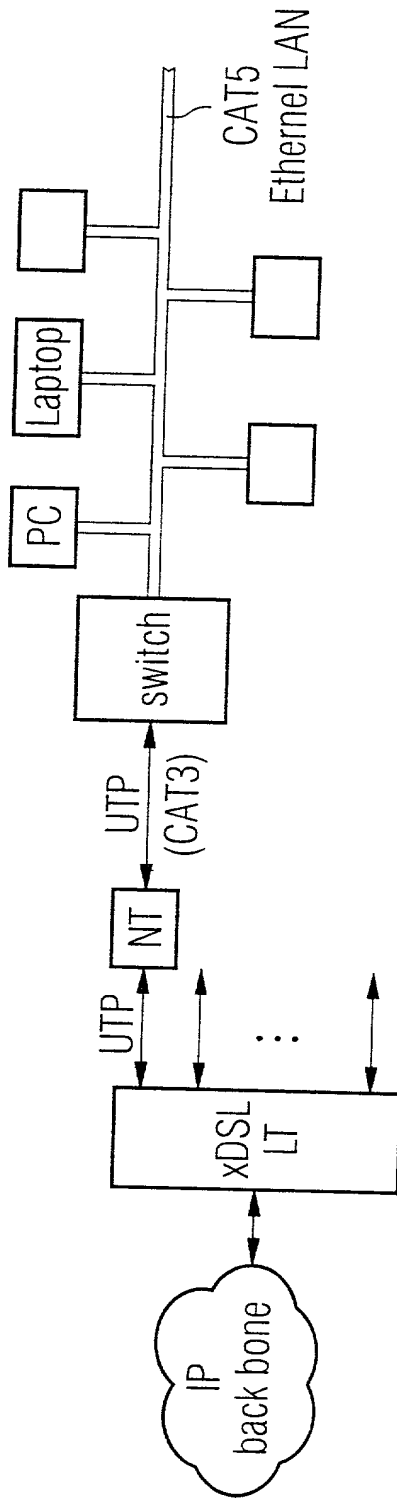
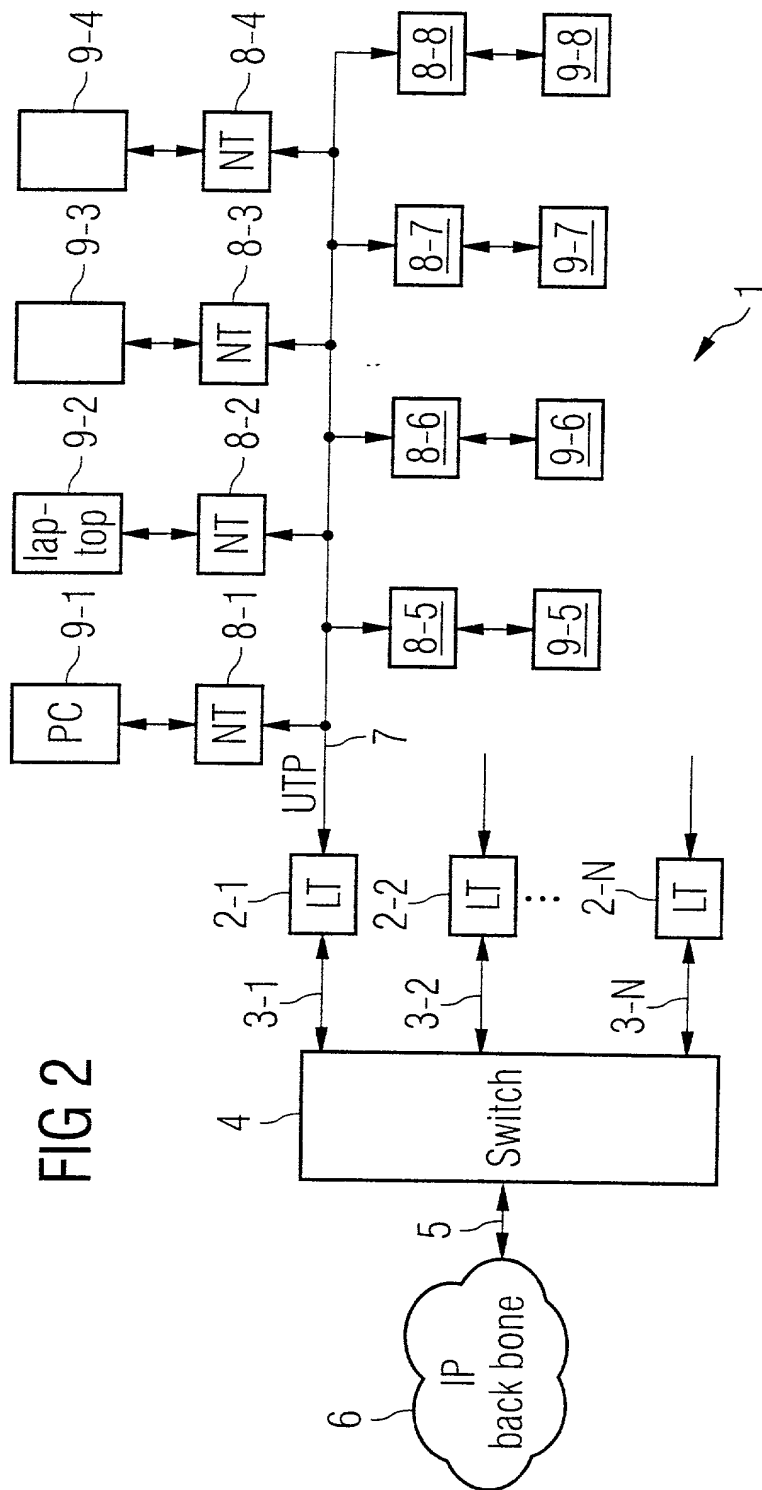
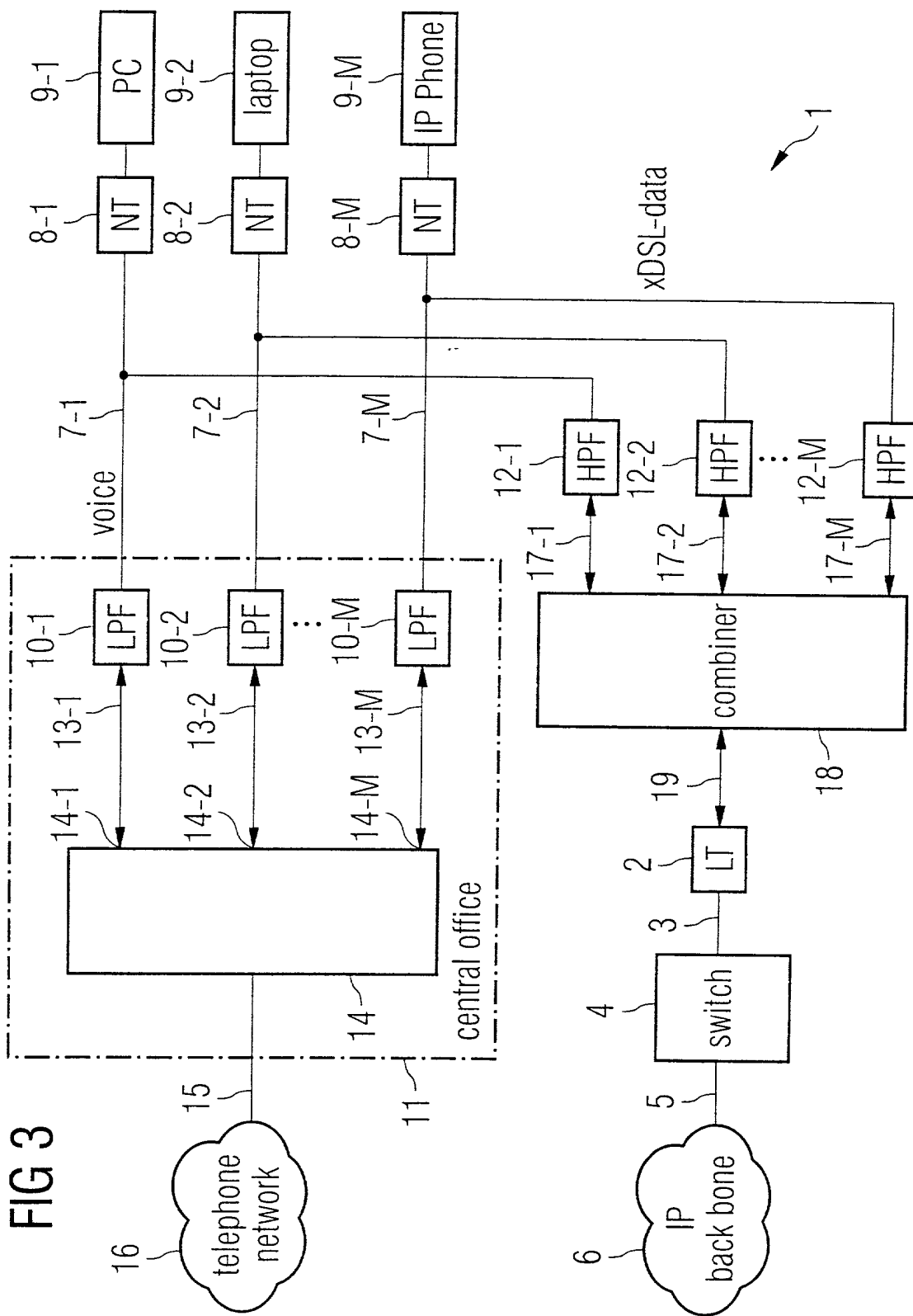


FIG 2





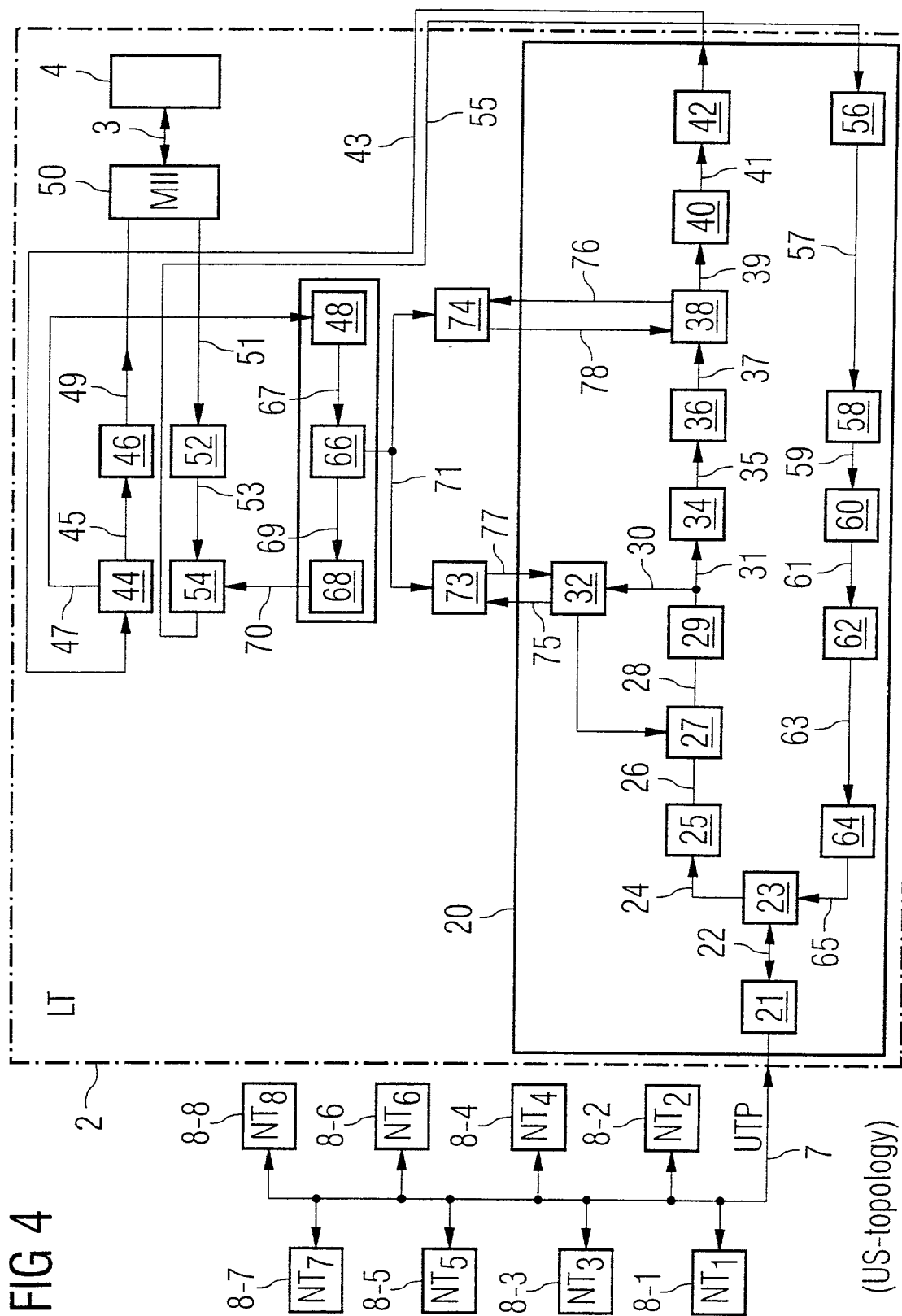


FIG 5

NT-status-table

NT	NT-adress	number of required time slots
NT ₁		
NT ₂		
⋮		
NT _N		

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FIG 6

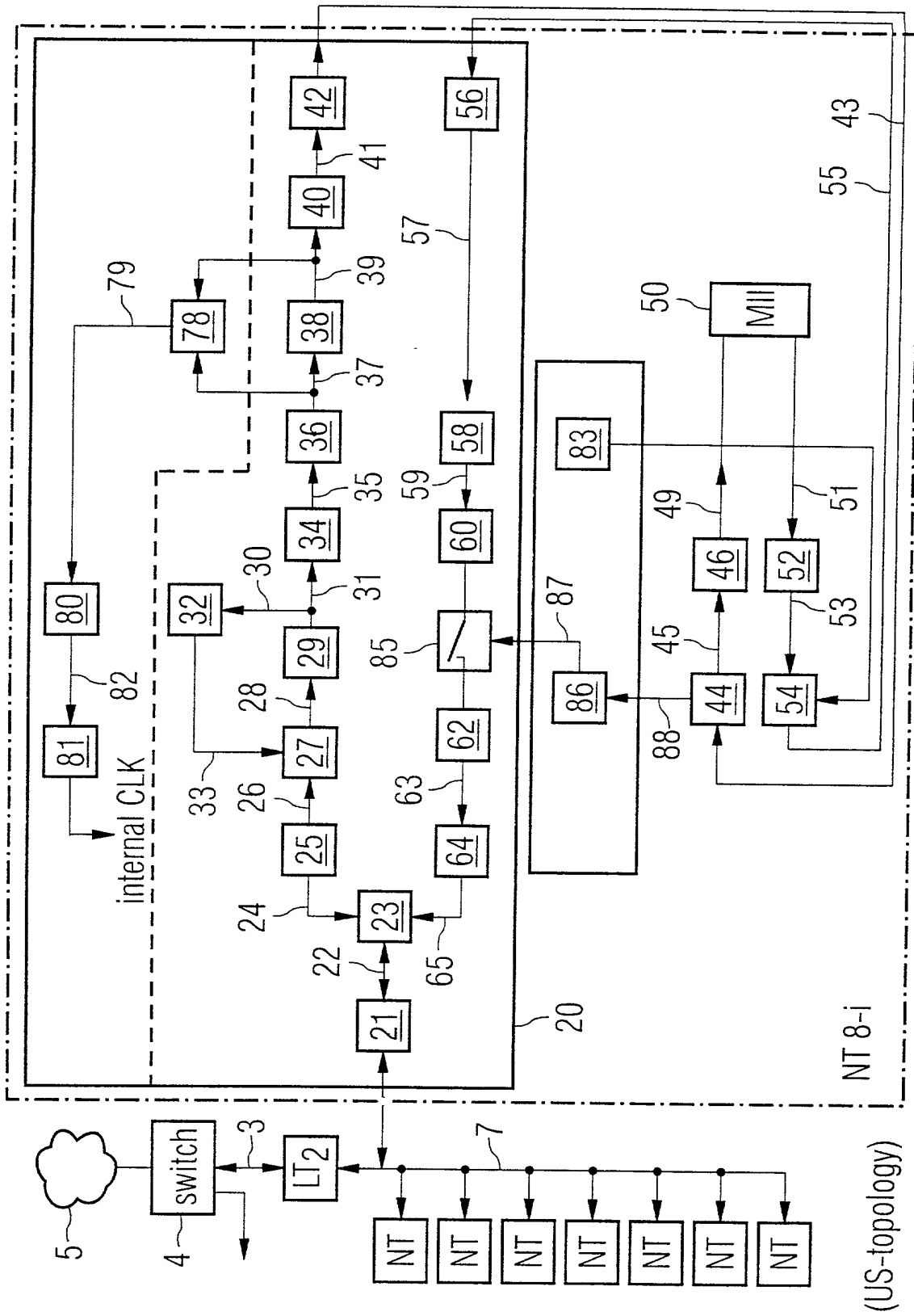


FIG 7a

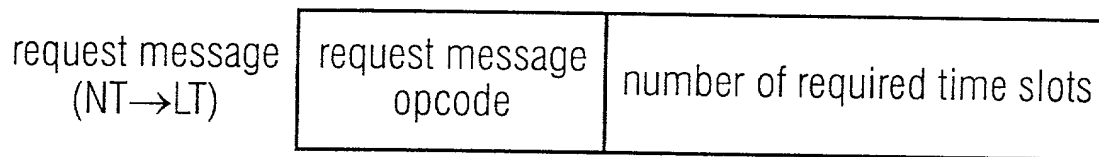


FIG 7b

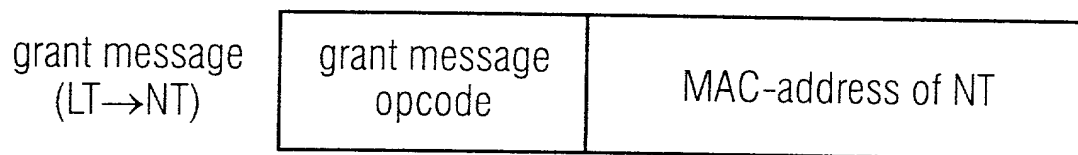


FIG 8

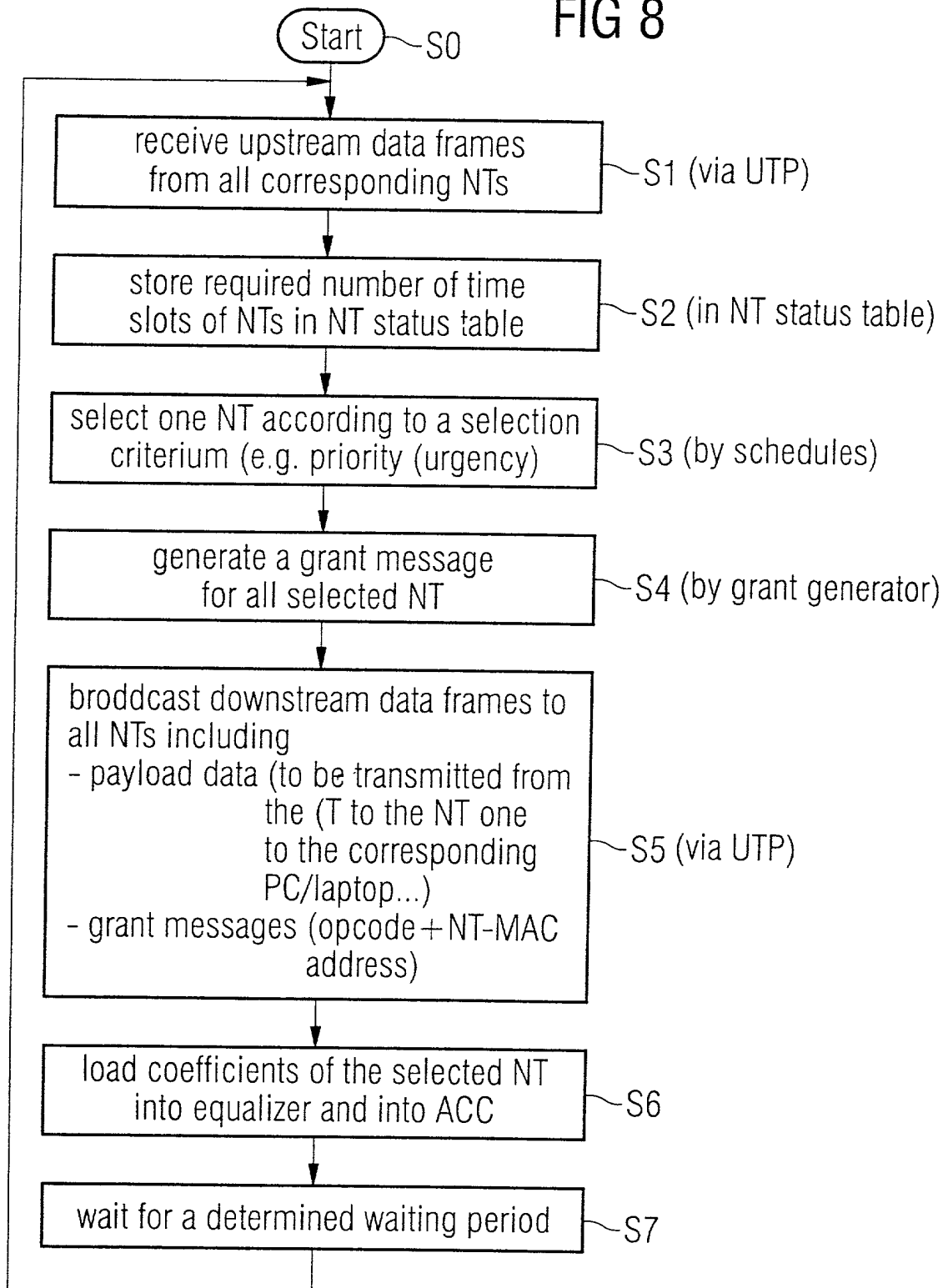


FIG 9

